

CET-MOS

https://www.cet-mos.com

10F., No. 176, Jiankang Rd., Chung-Ho Dist., New Taipei City, Taiwan 23585

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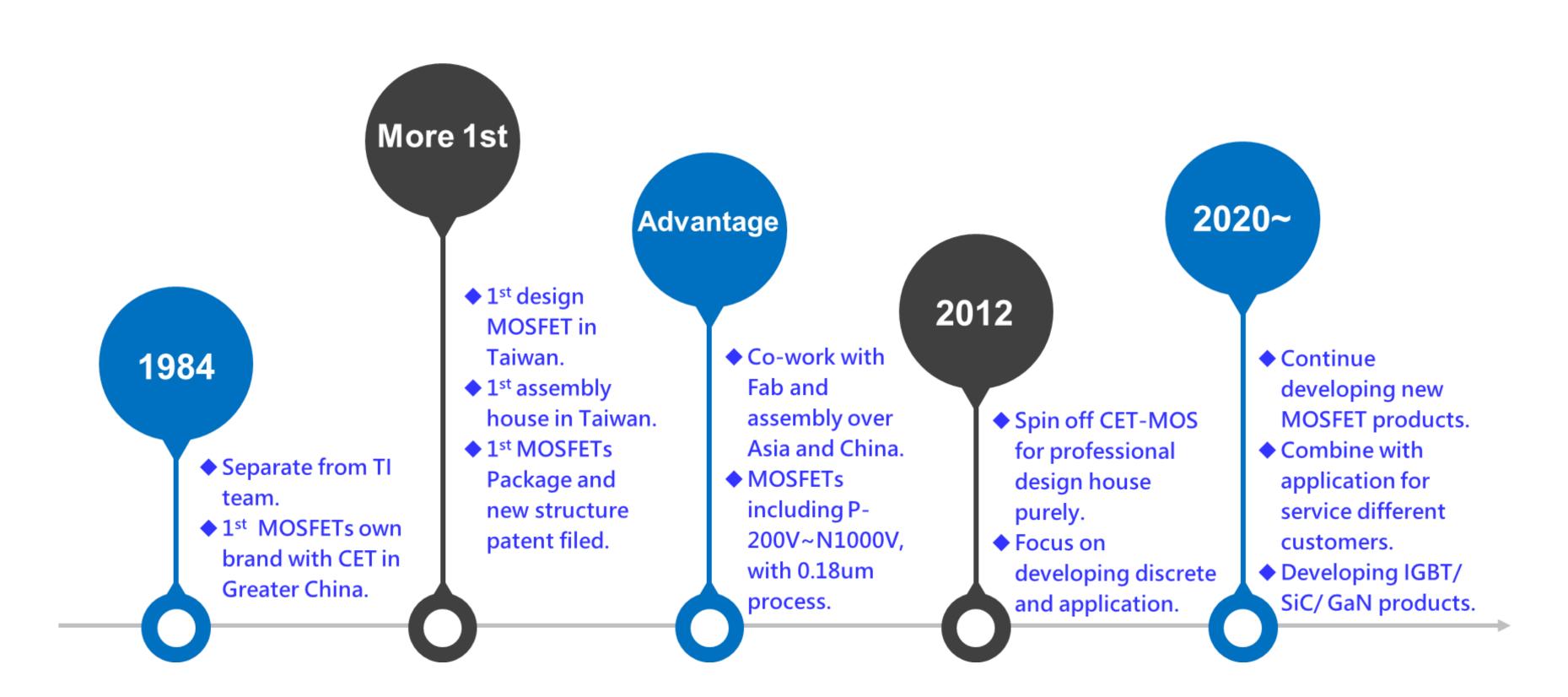
About US

CET-MOS Corporation (CET-MOS), as a spin-off company from Chino-Excel Technology Corporation (CET)

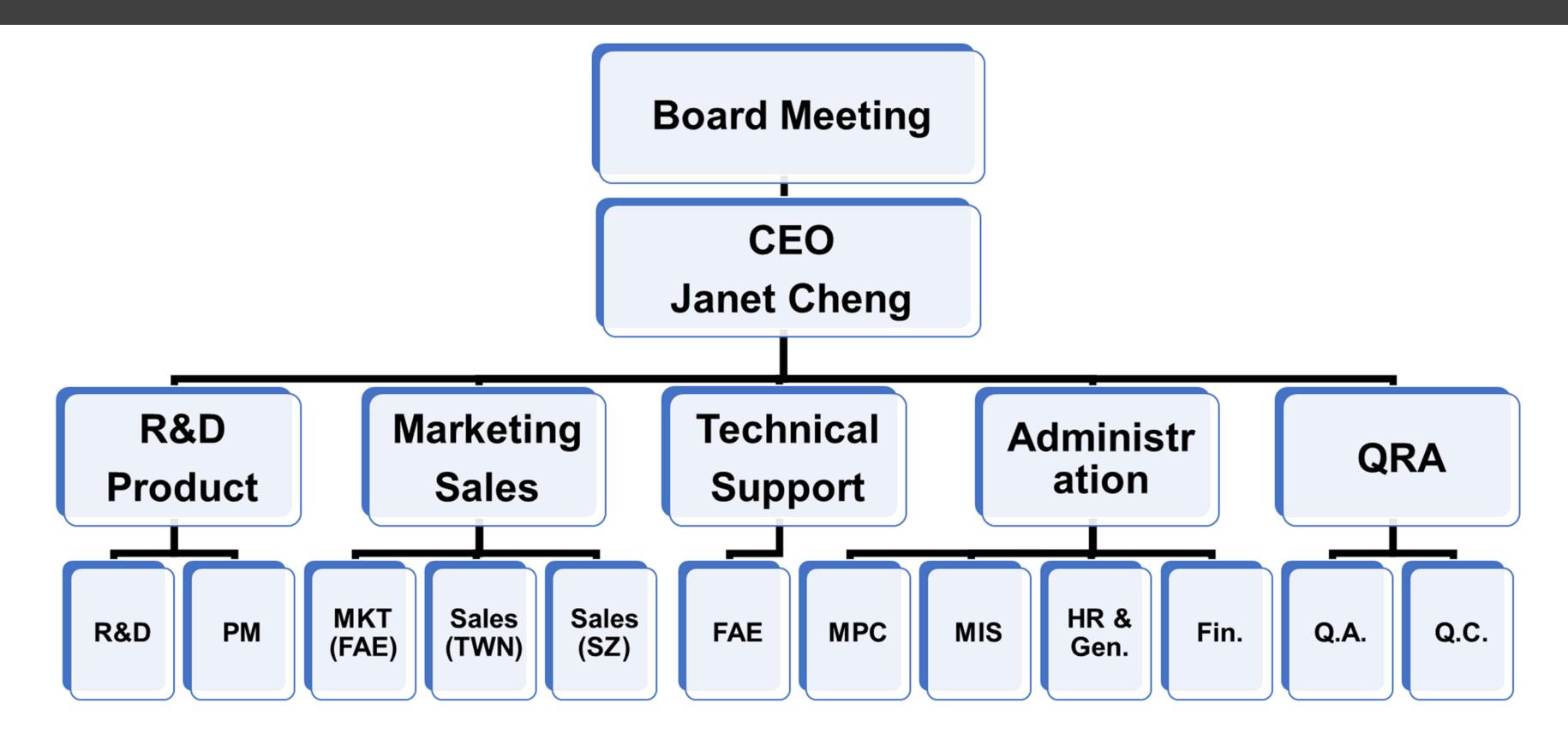
Inherited the technologies and experiences from CET over 25 years, successfully promote the first MOSFET to Taiwan market and keep as a pioneer in this field.

We are committed to excellence in design and manufacturing high-quality Power MOSFET.

Our Company



Organization



Our Vision

To offer best quality and reliability to the full satisfaction of customers' requirement is CET-MOS everlasting quality policy to pursue.

		RoHS								
ASSEMBLY PROCESS	ASSEMBLY TYPE	Pb	Cd	Hg	Cr+6	PBDEs	PBBs	Phthalates (DBP, BBP, DEHP, DIBP)	REACH (SVHC)	Non-Halogen
Epoxy Process	SOT, SOP, TSOP, TSSOP, DFN	V	V	V	V	V	V	V	V	v
Solder Process	TO, PPAK	Pb>1000ppm Exemptions AnnexIII 7(a)	V	V	V	V	V	V	V	V

DNV-GL

MANAGEMENT SYSTEM CERTIFICATE

Certificate No.: 6966-2003-AO-RGC- Initial certification d

Valid: 17 January, 2021 – 17 January, 2024

This is to certify that the management system of

CET-MOS Corporation

10F., No. 176, Jiankang Rd., Chung-Ho Dist., New Taipei City 23585, Taiwan and the sites as mentioned in the appendix accompanying this certificate

has been found to conform to the Quality Management System standard:

This certificate is valid for the following scope:

Design and Production Management of Metal Oxide Silicon Field Effect Transistors

Place and date: Shanghai, 21 December, 2020



For the Issuing office: DNV GL – Business Assurance Suite A, Building 9, No.1591 Hongqiao Road, Changning District, Shanghai 200336, P.R. China TEL + 86.2 1, 32799000

to the IAF MLA

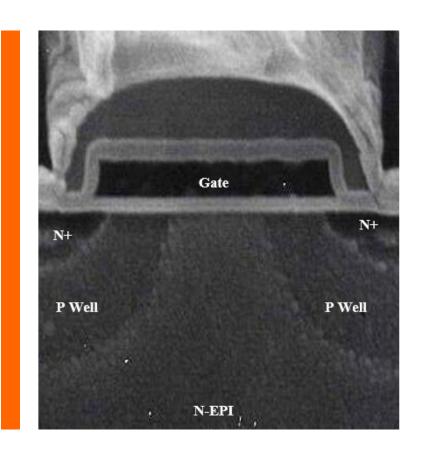
Zhu Hai Ming

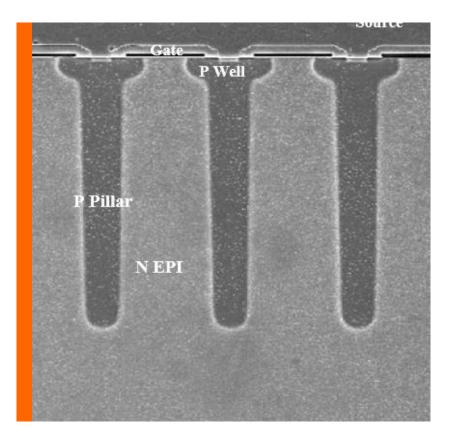
Lack of fulfilment of conditions as set out in the Certification Agreement may render this Certificate invalid,

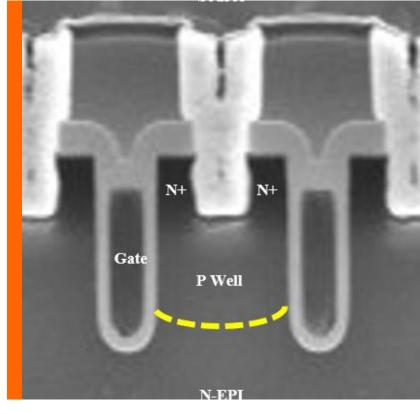
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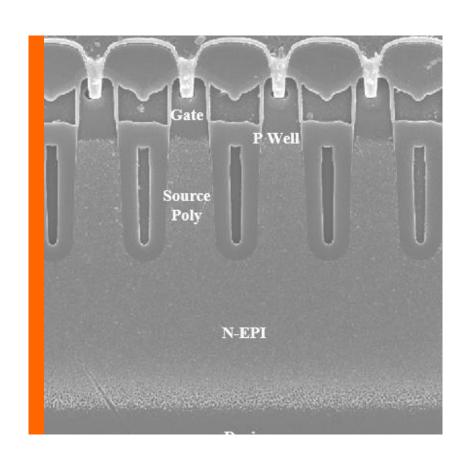
MOSFET

Variety technology for different product application









Planar Device

- High Voltage
- High Power
- Higher UIS
- Low Qgd

Super Junction Devices

- Planar Gate Structure
- High Drain-Source Breakdown
- Excellent Rds(on)*Qg
- High Switching Speed

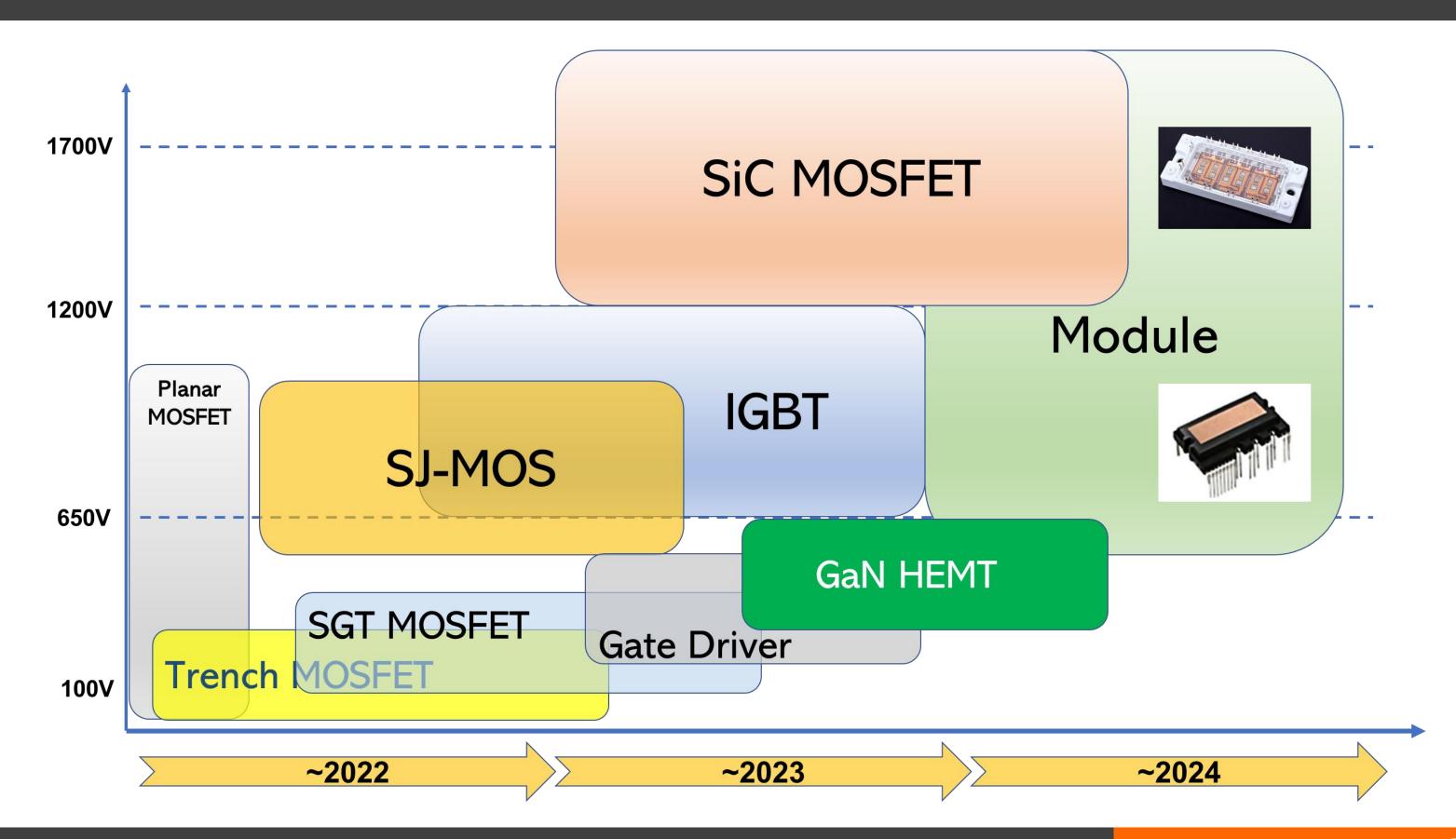
Trench Devices

- Higher Density
- Lower Rds(on)
- Lower Qgd
- Suitable Die Size
- Lower Cost

Split Gate Devices

- Trench Gate Structure
- Low & Medium Drain-Source Breakdown
- Ultra Low Rds(on)
- Lower Qgd/Qgs Ratio

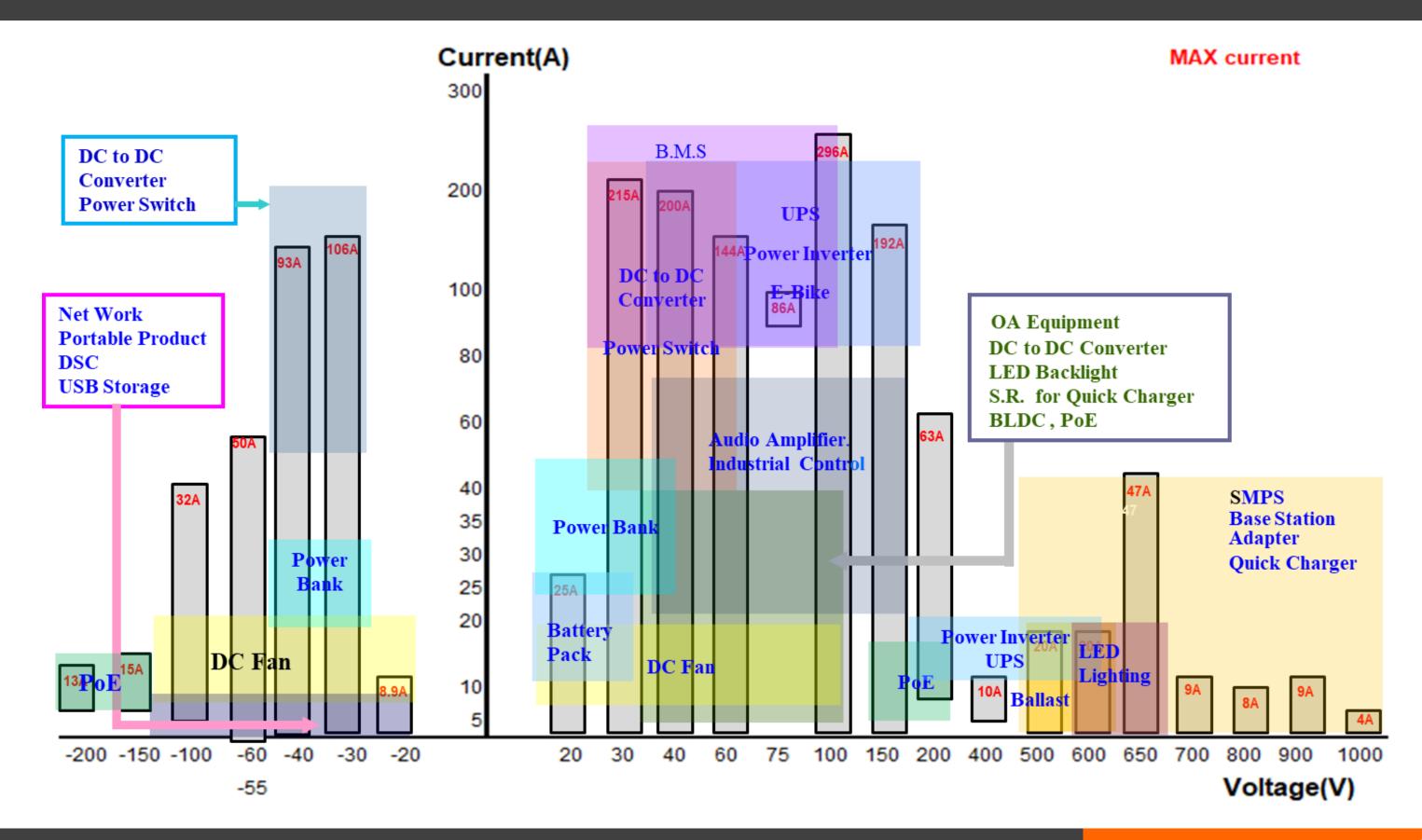
Products Development Roadmap



Variety Package Types

Products	LV/ MV MOSFETs	HV MOSFETs
Vds (V)	-200 ~ 500	500 ~ 1000
Packages	CET CET CET	CET CET

Application Roadmap



Top 10 Application

- Battery Protect
 - > Safety switches
- **♦** Inverter/ Converter
 - > DC/AC conversion and load switches
- **♦** Adapter/Charger/SPS
 - > Fly back/Forward /LLC switches
- **♦** LED Lighting
 - > Buck/Boost switches
- **♦ Network Communication**
 - DC/DC conversion & load switches
- **♦ UPS**
 - High Power/Bridge switches
- **♦** Motor/E-Bike
 - > High power Bridge switches
- **♦** Monitor
 - > Switches
- **♦ LED Display**
 - > Switches
- **♦ VGA**
 - DC/DC conversion & load switches





















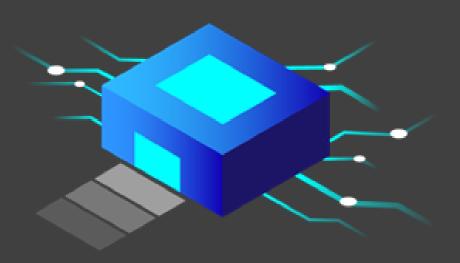






Business Model

- **♦** OBM
- OEM & ODM
- **♦ Turn-key Service**
- Customized
- **◆ Total Solution**



WHAT CAN WE DO?!







